



**AIM Summer Academy 2018 – July 23-27, MIT**  
**Integrated Photonics: Fundamentals, Applications and Implementation**  
**PIC Fundamentals Education Track**

	Monday	Tuesday	Wednesday	Thursday	Friday
Morning	8-8:50 AM <b>Registration, Welcome</b> (4-237) <i>S. Saini</i> <i>L.C. Kimerling</i>	8:30-10:20 AM <b>Integrated Photonics: Active Devices</b> (4-231) <i>J. Liu</i>	8:30-10:50 AM <b>PICs: Fabless Silicon Photonics Design Flow</b> (4-231) <i>S. Preble</i>	8:30-10:40 AM <b>PIC Fabrication: Design for Manufacturing</b> (4-231) <i>D. Boning</i>	8:30-11:30 AM <b>Design Presentations</b> (2-190)  Student Teams' Design Project Review
	9-9:50 AM <b>Photonics Fundamentals</b> (4-231) <i>L.C. Kimerling</i>				
	10-12 PM <b>Integrated Photonics: Passive Devices</b> (4-231) <i>J.J. Hu</i>	10:30-12 PM <b>Integrated Photonics: Chip Process Flow</b> (4-231) <i>L.C. Kimerling</i>	11-12 PM <b>APSUNY PDK &amp; MPW for Photonic Design</b> (4-231) <i>E. Timurdogan</i>	11-11:45 AM <b>EPDA Tool Overview</b> (32-082) Mentor	
				11:45-12 PM Group Photo	
	Lunch 12-1:15 PM (Virtual Lab Playtest, Mon-Weds)				11:30-12:30 PM <b>Conclusion</b> (2-190) - Education/Workforce - IPSR Roadmap <i>L.C. Kimerling</i>
Afternoon	1:15-2 PM <b>EPDA Tool Overview</b> Synopsys (4-265) Lumerical (4-257)	1:15-2 PM <b>EPDA Tool Overview</b> Synopsys (4-265) Lumerical (4-257)	1:15-2 PM <b>EPDA Tool Overview</b> Synopsys (4-265) Mentor (4-257)	1:15-2 PM <b>EPDA Tool Overview</b> (2-190) Cadence	
	2-4:40 PM <b>Applications Tutorial (Basics): Datacom, RF Photonics, Sensing, Augmented Imaging</b> (4-237) <i>L.C. Kimerling</i> <i>A. Agarwal</i> <i>K. Wada</i> <i>D. Prather</i>	2-3:20 <b>Applications Tutorial (Advanced)</b> (4-237) <i>L.C. Kimerling et al.</i>	2-3:50 PM <b>PIC Packaging</b> (2-190) <i>S. Preble</i>	2-2:30 PM <b>Introduction to AIM TAP Facility</b> (2-190) <i>E. White</i>	2:30-4:20 PM <b>PIC Optical &amp; Electrical Testing</b> (2-190) <i>J. Cardenas</i>
	4:45-6 PM <b>Design Team Breakout Session</b> (4-163, 4-231, 4-257, 4-265) <u>Define Problem</u>	3:30-6 PM <b>Design Team Breakout Session</b> (4-163, 4-231, 4-257, 4-265)	4-6 PM <b>Design Team Breakout Session</b> (4-163, 4-231, 4-257, 4-265)	4:30-6 PM <b>Design Team Breakout Session</b> (4-163, 4-231, 4-257, 4-265) <u>Finalize Design</u> - process integration	
		<u>Select Components</u> - Digital/Datacom - Analog/RF - Sensors - AR, 3D	<u>Plan Circuit Layout</u> - performance - constraints - options - PIC layout		
Evening			6-8 PM <b>Networking Dinner</b> (E52 – Samberg Conference Center)		