

AIM Photonics Academy
AIM Summer Academy 2018 – July 23-27, MIT
Integrated Photonics: Fundamentals, Applications and Implementation
PIC Fundamentals Education Track

	Monday	Tuesday	Wednesday	Thursday	Friday
Morning	8-8:50 AM Registration, Welcome <i>S. Saini</i> <i>L.C. Kimerling</i>	8:30-10:20 AM Integrated Photonics: Active Devices <i>J. Liu</i>	8:30-10:50 AM PICs: Fabless Silicon Photonics Design Flow <i>S. Preble</i>	8:30-10:50 AM PIC Fabrication: Design for Manufacturing <i>D. Boning</i>	8:30-11:30 AM Design Presentations Student Teams' Design Project Review
	9-9:50 AM Photonics Fundamentals <i>L.C. Kimerling</i>				
	10-12 PM Integrated Photonics: Passive Devices <i>J.J. Hu</i>	10:30-12 PM Integrated Photonics: Chip Process Flow <i>L.C. Kimerling</i>	11-12 PM APSUNY PDK & MPW for Photonic Design <i>E. Timurdogan</i>	11-12 PM EPDA Tool Overview Mentor	
	Lunch 12-1 PM				
Afternoon	1-1:50 PM EPDA Tool Overview Synopsys, Lumerical	1-1:50 PM EPDA Tool Overview Synopsys, Lumerical	1-1:50 PM EPDA Tool Overview PhoeniX Software (Synopsys), Mentor	1-1:50 PM EPDA Tool Overview Cadence	
	2-3:50 PM Tutorial: Datacom, RF Photonics <i>L.C. Kimerling</i> <i>D. Prather</i>	2-3:50 Tutorial: Sensing, Augmented Imaging <i>A. Agarwal,</i> <i>K. Wada</i>	2-3:50 PM PIC Packaging <i>S. Preble</i>	2-2:30 PM Introduction to AIM TAP Facility <i>E. White</i>	
	4-6 PM Design Team Breakout Session <u>Define Problem</u> - Digital/Datacom - Analog/RF - Sensors - AR, 3D	4-6 PM Design Team Breakout Session <u>Select Components</u> - Digital/Datacom - Analog/RF - Sensors - AR, 3D	4-6 PM Design Team Breakout Session <u>Plan Circuit Layout</u> - performance - constraints - options - PIC layout	2:30-4:20 PM PIC Optical & Electrical Testing <i>J. Cardenas</i>	
				4:30-6 PM Design Team Breakout Session <u>Finalize Design</u> - process integration	
Evening			6-8 PM Networking Dinner		