

AIM Photonics Academy
Summer Academy '17 – July 24-28, MIT
Silicon Photonics: Fundamentals, Applications and Implementation
Short Course Offerings and Schedule

	Monday	Tuesday	Wednesday	Thursday	Friday
Morning	8:30-9 AM Welcome (3-370) <i>S. Saini</i> <i>L.C. Kimerling</i>		8:30-10:15 AM PICs: Fabless Silicon Photonics Design Flow (3-370) <i>S. Preble</i>		8:30-9:00 AM AIM Photonics MPW (3-370) <i>B. Attaway</i>
	9-11 AM Fundamentals (3-370) <i>L.C. Kimerling</i>	9-11 AM Integrated Photonics: Active Devices (3-370) <i>J Liu</i>	10:15-11 AM Demo (3-370) PhoeniX Software	9-11 AM Design for Manufacturing (3-370) <i>D. Boning</i>	9-12 PM Presentations (3-370) Student Teams' Design Project Review
	11-12 PM Lab Demos Education & Practice Factory (35-011) Automated Waveguide Testing (E25-228) Recitation (3-370) <i>S. Saini</i>	11-12 PM Lab Demos Education & Practice Factory (35-011) Automated Waveguide Testing (E25-228) Recitation (3-370) <i>S. Saini</i>	11-12 PM Lab Demos Education & Practice Factory (35-011) Automated Waveguide Testing (E25-228) Recitation (3-370) <i>S. Saini</i>	11-12 PM Lab Demos Education & Practice Factory (35-011) Automated Waveguide Testing (E25-228) Recitation (3-370) <i>S. Saini</i>	Next Steps Silicon Photonics Cohort - Education - Workforce - Tech Roadmap
Lunch 12-1 PM					
Afternoon	1-3:15 PM Integrated Photonics: Passive Devices (3-370) <i>J.J. Hu</i>	1-1:15 PM Group Photo	1-3:15 PM PICs: Devices to Systems on a Chip (3-370) <i>S. Preble,</i> <i>M. Popovic</i>	1-1:45 PM Demo (3-370) Mentor	
		1:15-2 PM Demo (3-370) Lumerical		1:45-2:30 PM Demo (3-370) Coventor	
		2-3:30 PM Integrated Photonics: Chip Process Flow (3-370) <i>L.C. Kimerling</i> <i>J. Liu</i>		2:30-3:45 PM Manufacturing in Practice: SPC (3-370) <i>K. McComber</i>	
	3:15-4 PM Demo (3-370) Synopsys/RSoft	3:15-4 PM Recap (3-370) <i>S. Saini</i>	3:15-4 PM Demo (3-370) Cadence	3:45-4:15 PM Lab Debriefing (3-370) <i>L. Kimerling</i>	
	4-5:30 PM Design Team Breakout Session (4-153, 4-159, 4-257, 4-265) <u>Define Problem</u> - Digital/Datacom - Analog/RF - Sensors - AR, 3D	4-5:30 PM Design Team Breakout Session (4-153, 4-159, 4-257, 4-265) <u>Select Components</u> - Digital/Datacom - Analog/RF - Sensors - AR, 3D	4-5:30 PM Design Team Breakout Session (4-153, 4-159, 4-257, 4-265) <u>Plan Circuit Layout</u> - performance - constraints - options - PIC layout	4:15-6 PM Design Team Breakout Session (4-153, 4-159, 4-257, 4-265) <u>Finalize Design</u> - process integration	
		6-8 PM Networking Dinner (E52 - Samberg Conference Center)			