

**AIM Photonics Academy  
Winter Academy 2018 – January 16-18, MIT**

**Silicon Photonics: Fundamentals of Chip Fabrication**

	Monday	Tuesday	Wednesday	Thursday	Friday
Morning	<b>Martin Luther King Day</b>	8:30-9 AM <b>Welcome</b> (66-110) <i>S. Saini</i> <i>L.C. Kimerling</i>		8:30-10:20 AM <b>PIC Fabrication: Design for Manufacturing</b> (66-110) <i>D. Boning</i>	
		9-10:00 AM <b>Fundamentals</b> (66-110) <i>L.C. Kimerling</i>	9-11 AM <b>Integrated Photonics: Active Devices</b> (66-110) <i>J Liu</i>	10:20-12:00 PM <b>Design Lab Simulation &amp; Layout Verification, pt. 1</b> (66-110) <i>K. Wada</i>	
		10-12:00 PM <b>Integrated Photonics: Passive Devices</b> (66-110) <i>J.J. Hu</i>	11-11:45 AM <b>AIM PDK &amp; MPW for Photonic Design</b> (66-110) <i>P. Goetz</i>		
		11:45-12:00 PM <b>Group Photo</b>			
Lunch 12-1 PM					
Afternoon		1-2:00 PM <b>Integrated Photonics: Chip Process Flow</b> (66-110) <i>L.C. Kimerling</i>	1-3:00 PM <b>PICs: Fabless Silicon Photonics Design Flow</b> (66-110) <i>S. Preble</i>	1:00-2:45 PM <b>Design Lab Simulation &amp; Layout Verification, pt. 2</b> (66-110) <i>K. Wada</i>	
	2-5:30 PM <b>Design Lab Device Component Modeling</b> (66-110) <i>A. Agarwal, J. Hu</i>	3-6:30 PM <b>Design Lab Simulation &amp; Layout</b> (66-110) <i>M. Glick, L.C. Kimerling</i>	EPDA experts - Lumerical - Mentor		
	EPDA experts - Synopsys - Coventor	EPDA experts - PhoeniX Software - Cadence	2:45-3:45 PM <b>Design Lessons Learned, PIC Roadmap</b> (66-110) IPSR International <i>L.C. Kimerling</i>		
Evening		6:45-8:30 PM <b>Networking Dinner</b> (E52, 6 <sup>th</sup> floor - Samberg Conference Center)			